IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of:

Confirmation No.: 5951

Kenichi ADACHI et al.

Group Art Unit: Not Yet Assigned

Application No.: 10/575,041

Examiner: Not Yet Assigned

Filing Date: April 5, 2006

Attorney Docket No.: 545/72

For: Processor Controlling Performance In Accordance With A Chip Temperature,

Information Processing Apparatus, and Method

of Controlling Processor

INFORMATION DISCLOSURE STATEMENT

VIA EFS

Mail Stop: Amendment Commissioner of Patents P.O. Box 1450 Alexandria, VA 22313-1450

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Applicant respectfully submits this Information Disclosure Statement, identification of the information, and copies thereof if required pursuant to 37 C.F.R. §§ 1.56, 1.97 and 1.98 in order to comply with the duty of disclosure. The enclosed Form PTO SB/08A/B identifies the references of which Applicant is aware. The Examiner is respectfully requested to consider the information herein and make it of record in the subject application.

By checking this paragraph, this Information Disclosure Statement is being filed pursuant to 37 C.F.R. § 1.97(b) within three months of the filing date of the application or before the application has received a substantive Office Action. Accordingly, no fee is necessary; however, should the Patent Office determine that a fee is due, the Commissioner is authorized to charge any such fee to Deposit Account No. 11-0223.

By checking this paragraph, this Information Disclosure Statement is being filed pursuant to 37 C.F.R. § 1.97(c) before the mailing date of a Final Action, a Notice of Allowance, or close of prosecution. Accordingly included herewith is:

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U. S. PATENT DOCUMENTS

Publication Date MM-DD-YYYY

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STATEMENT BY APPLICANT				First Named Inventor	Kenichi Adachi		
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